

METHODS AND SYSTEMS FOR SENDING SIDE-CHANNEL

DATA DURING DATA INACTIVE PERIOD

Abstract of the Disclosure

[0098] The present invention relates to a serial interface transmission system with more than one data line, in which the transmitted data has in-band and out-of-band characters. More particularly, the present invention relates to methods and systems for sending side channel data over a high-speed digital communications link, e.g., a video link. One embodiment of the invention provides a high-speed digital transmitter capable of sending side channel data. The transmitter includes a channel zero encoder, a multiplexer, data enable out (DE_{out}) control logic, and a channel one encoder. The channel one encoder receives input from the channel one multiplexer and the channel one DE_{out} control logic. Another embodiment of the invention provides a high-speed digital receiver capable of receiving side channel data. The receiver includes a channel zero decoder, a channel one decoder, DEI signal and FIFO control signal recovery logic, and a channel one de-multiplexer. The DEI signal and FIFO control signal recovery logic receives input from the channel one decoder. Similarly, the channel one demultiplexer receives input from the channel one decoder.

19570/05146/DOCS/1160202.8